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# What's New in NI LabVIEW Embedded Module for ADI Blackfin Processors 2.0

## Overview

The LabVIEW Embedded Module for Blackfin Processors provides an integrated out-of-the-box programming experience for embedded system design. LabVIEW generates code to target the low-power high-performance Blackfin processor. With the 2.0 version of the module, a set of new features have been introduced to help with development, debugging, and deployment. The following document outlines some of the new features that have been introduced in this version.

**Support for additional Blackfin variants including the low-cost ADSP-BF531** - The LabVIEW Embedded Module for ADI Blackfin processors can now deploy to the following Blackfin variants:

- ASDP-BF531
  
- ASDP-BF532
- ASDP-BF533
- ASDP-BF534
- ASDP-BF536
- ASDP-BF537
- ASDP-BF538



**36% average improvement in run-time performance of generated code** - Applications that were developed using the LabVIEW Embedded Module for Blackfin Processors version 1.0 will have an average of 36% performance improvement when brought into version 2.0.



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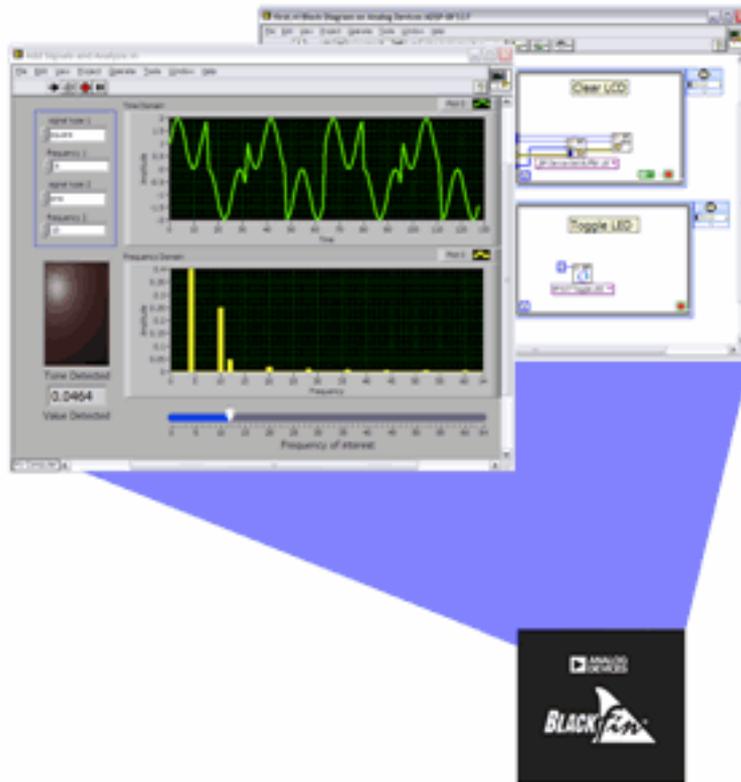


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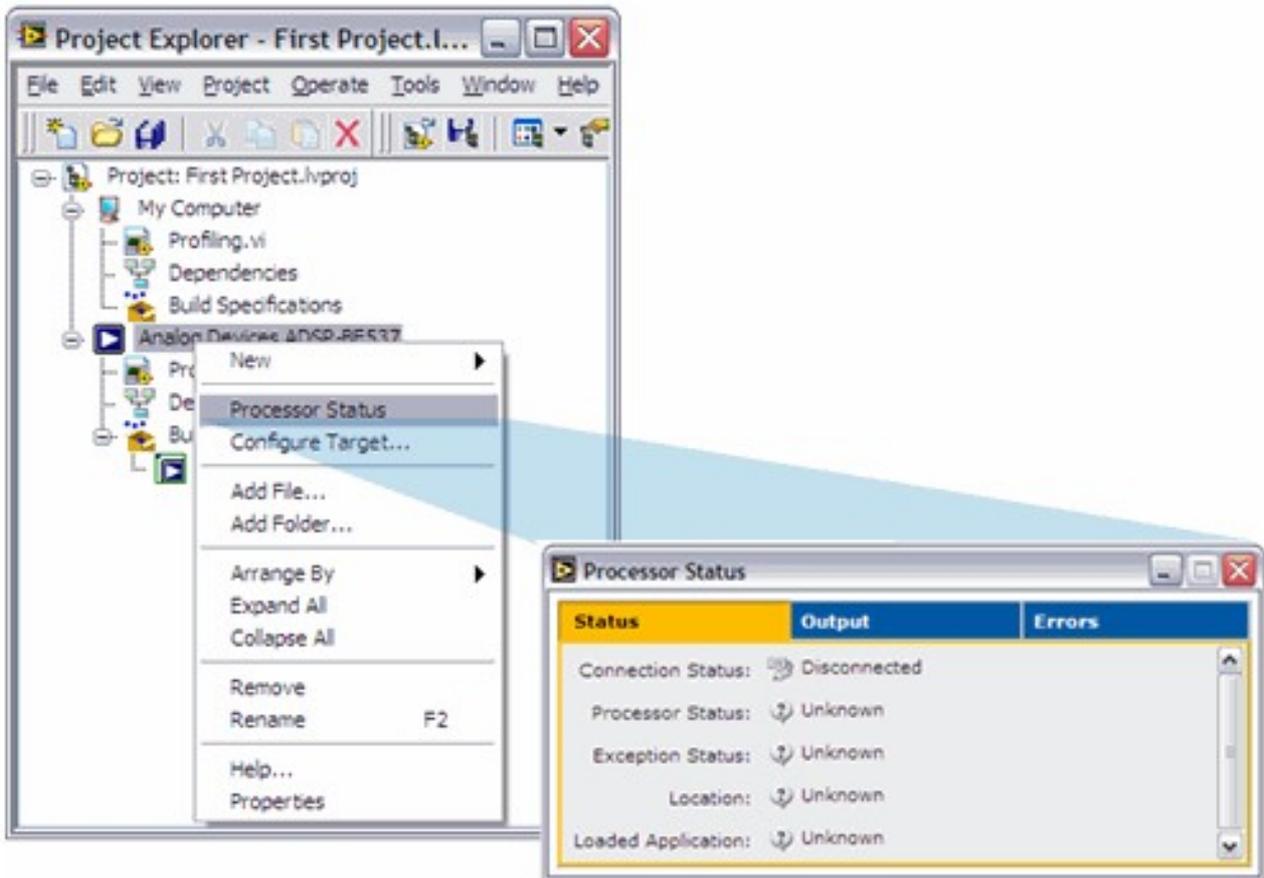
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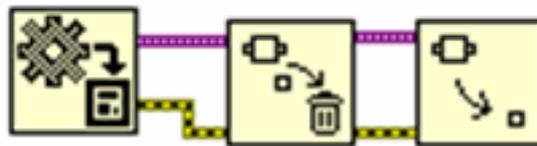




**Integration with LabVIEW 8.2 and VisualDSP++ 4.5** - Take advantage of the LabVIEW 8.2 project window for organizing your source files, libraries, and supporting documents. In addition, you can simultaneously debug applications that are running on the Host PC, a LabVIEW FPGA target, and the Blackfin Processor.

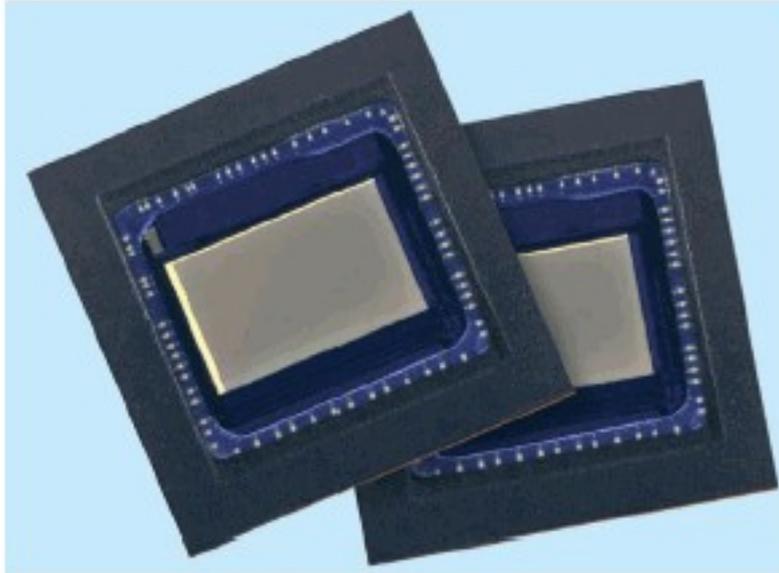


**Additional LabVIEW datatype support for more compatibility with existing LabVIEW code** - With support for more datatypes like the timestamp, variants, and the dynamic datatype, more of your existing LabVIEW VIs will be available for reuse on the Blackfin Processor.



**New Drivers:**

- Two-Wire Interface (also called I<sup>2</sup>C) Driver
- Micron MT9V022 CMOS Image Sensor Driver

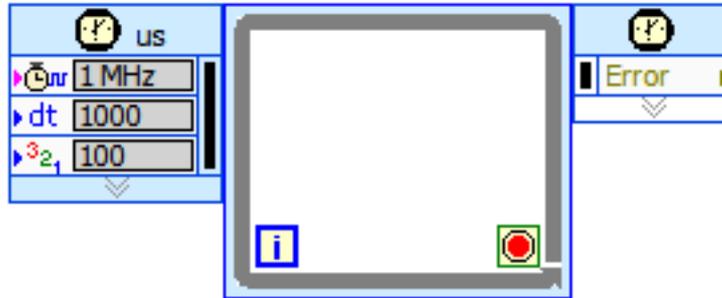


**Static IP address support including TCP/IP debugging** - this feature adds the ability to assign a static IP address to the Blackfin for applications where network connectivity is required but a DHCP Server is not available.

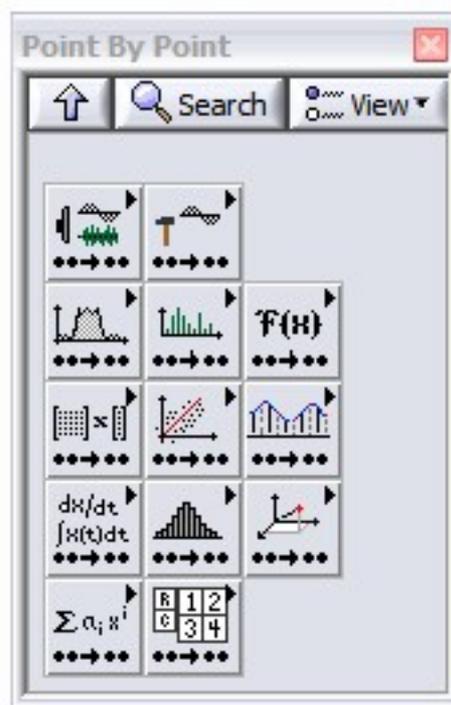
Enable lwIP TCP/IP support

<input type="checkbox"/> Use DHCP	<input checked="" type="checkbox"/> MAC in target memory
IP address <input type="text" value="10.0.0.1"/>	MAC address <input type="text"/>
Subnet mask <input type="text" value="255.255.0.0"/>	MAC location <input type="text" value="x0"/>
Gateway <input type="text"/>	

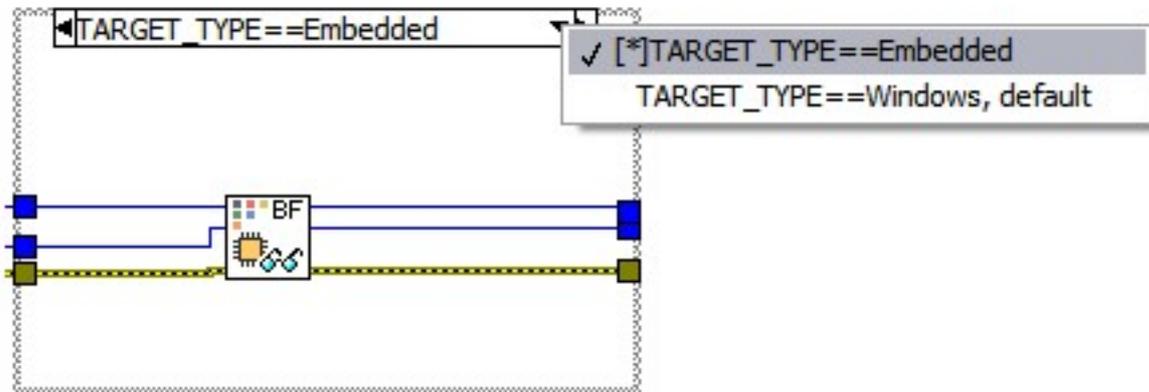
**Timed Loop support for MHz loop rates** - with the new timing sources for timed loop, you can achieve microsecond timing accuracy for filtering and control applications in high priority threads.



**New Point-by-point Analysis VIs for Single Point Control Applications** - for control applications that require single point I/O like PID, there are now a set of point-by-point analysis VIs for signal processing and analysis like filtering and FFTs.



**New structures including conditional disable for target specific code generation** - now you can now write a single VI that executes different code on an embedded target than it does on



[Click here to order the Evaluation Kit!](#)